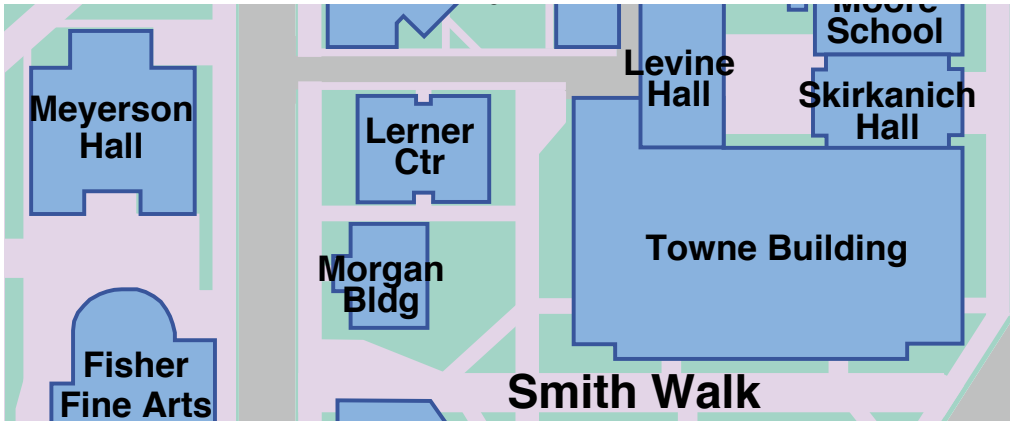
Lecture 14

CIS 341: COMPILERS

Announcements

- Midterm Exam:
  Location:
  Meyerson Hall B3
  MEYH B3

  Thursday, March 5
  noon-1:30



- HW4: Oat v. 1 Frontend
  - Due: March 26th at 11:59pm
  - Counts as 2x project
  - *Start early!*

See lec14.zip

OAT V.0

Inference Rules

- We can read a judgment `G;L` $\vdash$ e : t as "the expression e is well typed and has type t"
- For any environment G, expression e, and statements s_1, s_2.

$$\texttt{G;L;rt} \vdash \texttt{if}\ (e)\ s_1\ \texttt{else}\ s_2$$

holds if `G` ;L$\vdash$ e : bool and `G;L;rt` $\vdash s_1$ and `G;L;rt` $\vdash s_2$ all hold.

- More succinctly: we summarize these constraints as an *inference rule*:

Premises

$$\texttt{G;L} \vdash e : \text{bool} \qquad \texttt{G;L;rt} \vdash s_1 \qquad \texttt{G;L;rt} \vdash s_2$$

Conclusion

$$\texttt{G;L;rt} \vdash \texttt{if}\ (e)\ s_1\ \texttt{else}\ s_2$$

- This rule can be used for *any* substitution of the syntactic metavariables `G`, e, s_1 and s_2.

Checking Derivations

- A *derivation* or *proof tree* has (instances of) judgments as its nodes and edges that connect premises to a conclusion according to an inference rule.
- Leaves of the tree are *axioms* (i.e. rules with no premises)
  - Example: the INT rule is an axiom
- Goal of the type checker: verify that such a tree exists.
- Example1: Find a tree for the following program using the inference rules in oat0-defn.pdf:

```
int x1 = 0;
int x2 = x1 + x1;
x1 = x1 – x2;
return(x1);
```

Example2: There is no tree for this ill-scoped program:

```
int x2 = x1 + x1;
return(x2);
```

Example Derivation

```
int x1 = 0;
int x2 = x1 + x1;
x1 = x1 – x2;
return(x1);
```

$$\dfrac{\dfrac{\mathcal{D}_1 \quad \mathcal{D}_2 \quad \mathcal{D}_3 \quad \mathcal{D}_4}{G_0\,;\cdot\,;\texttt{int} \vdash \texttt{int}\ x_1 = 0; \texttt{int}\ x_2 = x_1 + x_1; x_1 = x_1 - x_2; \texttt{return}\ x_1; \Rightarrow \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}\ [\textsc{stmts}]}{\vdash \texttt{int}\ x_1 = 0; \texttt{int}\ x_2 = x_1 + x_1; x_1 = x_1 - x_2; \texttt{return}\ x_1;}\ [\textsc{prog}]$$

Example Derivation

$$\mathcal{D}_1 \quad = \quad \dfrac{\dfrac{\dfrac{\dfrac{}{G_0\,;\cdot \vdash 0 : \texttt{int}}\,[\textsc{int}]}{G_0\,;\cdot \vdash 0 : \texttt{int}}\,[\textsc{const}]}{G_0\,;\cdot \vdash \texttt{int}\ x_1 = 0 \Rightarrow \cdot, x_1 : \texttt{int}}\,[\textsc{decl}]}{G_0\,;\,\cdot\,;\texttt{int} \vdash \texttt{int}\ x_1 = 0\,; \Rightarrow \cdot, x_1 : \texttt{int}}\,[\textsc{sdecl}]$$

$$\mathcal{D}_2 \quad = \quad \dfrac{\dfrac{\dfrac{\dfrac{}{\vdash + : (\texttt{int},\texttt{int}) \to \texttt{int}}\,[\textsc{add}] \quad \dfrac{x_1 : \texttt{int} \in \cdot, x_1 : \texttt{int}}{G_0\,;\cdot, x_1 : \texttt{int} \vdash x_1 : \texttt{int}}\,[\textsc{var}] \quad \dfrac{x_1 : \texttt{int} \in \cdot, x_1 : \texttt{int}}{G_0\,;\cdot, x_1 : \texttt{int} \vdash x_1 : \texttt{int}}\,[\textsc{var}]}{G_0\,;\cdot, x_1 : \texttt{int} \vdash x_1 \ + \ x_1 : \texttt{int}}\,[\textsc{bop}]}{G_0\,;\cdot, x_1 : \texttt{int}\,;\texttt{int} \vdash \texttt{int}\ x_2 = x_1 \ + \ x_1\,; \Rightarrow \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}\,[\textsc{decl}]}{G_0\,;\cdot, x_1 : \texttt{int}\,;\texttt{int} \vdash \texttt{int}\ x_2 = x_1 \ + \ x_1\,; \Rightarrow \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}\,[\textsc{sdecl}]$$

Example Derivation

$$\mathcal{D}_3 \quad = \quad \dfrac{x_1 : \texttt{int} \in \cdot, x_1 : \texttt{int}, x_2 : \texttt{int} \qquad \dfrac{\dfrac{}{\vdash \texttt{-} : (\texttt{int}, \texttt{int}) \to \texttt{int}}\ [\textsc{add}] \quad \dfrac{x_1 : \texttt{int} \in \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}{G_0\,;\cdot, x_1 : \texttt{int}, x_2 : \texttt{int} \vdash x_1 : \texttt{int}}\ [\textsc{var}] \quad \dfrac{x_2 : \texttt{int} \in \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}{G_0\,;\cdot, x_1 : \texttt{int}, x_2 : \texttt{int} \vdash x_2 : \texttt{int}}\ [\textsc{var}]}{G_0\,;\cdot, x_1 : \texttt{int}, x_2 : \texttt{int} \vdash x_1 \texttt{ - } x_2 : \texttt{int}}\ [\textsc{bop}]}{G_0\,;\cdot, x_1 : \texttt{int}, x_2 : \texttt{int}\,;\texttt{int} \vdash x_1 \texttt{ = } x_1 \texttt{ - } x_2\,; \Rightarrow \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}\ [\textsc{assn}]$$

$$\mathcal{D}_4 \quad = \quad \dfrac{\dfrac{x_1 : \texttt{int} \in \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}{G_0\,;\cdot, x_1 : \texttt{int}, x_2 : \texttt{int} \vdash x_1 : \texttt{int}}\ [\textsc{var}]}{G_0\,;\cdot, x_1 : \texttt{int}, x_2 : \texttt{int}\,;\texttt{int} \vdash \texttt{return}\ x_1\,; \Rightarrow \cdot, x_1 : \texttt{int}, x_2 : \texttt{int}}\ [\textsc{Ret}]$$

Why Inference Rules?

- They are a compact, precise way of specifying language properties.
  - E.g. ~20 pages for full Java vs. 100's of pages of prose Java Language Spec.
- Inference rules correspond closely to the recursive AST traversal that implements them
- Compiling in a context is nothing more an "interpretation" of the inference rules that specify typechecking*: $[\![C \vdash e : t]\!]$
  - Compilation follows the typechecking judgment
- Strong mathematical foundations
  - The "Curry-Howard correspondence": Programming Language ~ Logic, Program ~ Proof, Type ~ Proposition
  - See CIS 500 next Fall if you're interested in type systems!

*Here (and later) we'll write context C for `G;L`, the combination of the global and local contexts.

Compilation As Translating Judgments

- Consider the source typing judgment for source expressions:

$$C \vdash e : t$$

- How do we interpret this information in the target language?

$$[\![C \vdash e : t]\!] = \ ?$$

- $[\![t]\!]$ is a target type
- $[\![e]\!]$ translates to a (potentially empty) sequence of instructions, that, when run, computes the result into some operand

- INVARIANT: if $[\![C \vdash e : t]\!] = ty, operand, stream$
  then the type (at the target level) of the operand is $ty = [\![t]\!]$

Example

- C ⊢ 341 + 5 : int what is ⟦ C ⊢ 341 + 5 : int⟧ ?

⟦ ⊢ 341 : int ⟧ = `(i64, Const 341, [])` ⟦⊢ 5 : int⟧ = `(i64, Const 5, [])`

⟦C ⊢ 341 : int⟧ = `(i64, Const 341, [])` ⟦C ⊢ 5 : int⟧ = `(i64, Const 5, [])`

⟦C ⊢ 341 + 5 : int⟧ = `(i64, %tmp, [%tmp = add i64 (Const 341) (Const 5)])`

What about the Context?

- What is ⟦C⟧?
- Source level C has bindings like: x:int, y:bool
  - We think of it as a finite map from identifiers to types

- What is the interpretation of C at the target level?

- ⟦C⟧ maps source identifiers, "x" to source types and ⟦x⟧

- What is the interpretation of a variable ⟦x⟧ at the target level?
  - How are the variables used in the type system?

$$\frac{x : t \in L}{G ; L \vdash x : t}\ \textbf{TYP\_VAR}$$

as expressions
(which denote values)

$$\frac{x : t \in L \quad G ; L \vdash exp : t}{G ; L ; rt \vdash x = exp ; \Rightarrow L}\ \textbf{TYP\_ASSN}$$

as addresses
(which can be assigned)

Interpretation of Contexts

- ⟦C⟧ = a map from source identifiers to types and target identifiers

- INVARIANT:
  x:t ∈ C means that

  (1) lookup ⟦C⟧ x = (t, `%id_x`)
  (2) the (target) type of `%id_x` is ⟦t⟧* (a pointer to ⟦t⟧)

Interpretation of Variables

- Establish invariant for expressions:

$$\left[\!\!\left[\frac{x : t \in L}{G;L \vdash x : t}\ \textbf{TYP\_VAR} \right]\!\!\right] = \texttt{(\%tmp, [\%tmp = load i64* \%id\_x])}$$

as expressions
(which denote values)

where (**i64**, **%id_x**) = lookup ⟦L⟧ x

- What about statements?

$$\left[\!\!\left[\frac{x : t \in L \quad G;L \vdash exp : t}{G;L;rt \vdash x = exp; \Rightarrow L}\ \textbf{TYP\_ASSN} \right]\!\!\right] = \text{stream @}$$

`[store ⟦t⟧ opn, ⟦t⟧* %id_x]`

as addresses
(which can be assigned)

where (t, **%id_x**) = lookup ⟦L⟧ x
and ⟦G;L ⊢ exp : t⟧ = (⟦t⟧, **opn**, stream)

Other Judgments?

- Statement:
  ⟦C; rt ⊢ stmt ⇒ C′⟧ = ⟦C′⟧ , stream

- Declaration:
  ⟦G;L ⊢ t x = exp ⇒ G;L,x:t ⟧ = ⟦G;L,x:t⟧, stream

  INVARIANT: stream is of the form:

```
stream' @
[ %id_x = alloca ⟦t⟧;
  store ⟦t⟧ opn, ⟦t⟧* %id_x ]
```

  and ⟦G;L ⊢ exp : t ⟧ = (⟦t⟧, `opn`, stream′)

- Rest follow similarly

COMPILING CONTROL

Translating while

- Consider translating "`while(e) s`":
  - Test the conditional, if true jump to the body, else jump to the label after the body.

`⟦C;rt ⊢ while(e) s ⇒ C'⟧ = ⟦C'⟧`,

```
lpre:
    opn = ⟦C ⊢ e : bool⟧
    %test = icmp eq i1 opn, 0
    br %test, label %lpost, label %lbody
lbody:
     ⟦C;rt ⊢ s ⇒ C'⟧
     br %lpre
lpost:
```

- Note: writing `opn = ⟦C ⊢ e : bool⟧` is pun
  - translating ⟦C ⊢ e : bool⟧ generates *code* that puts the result into `opn`
  - In this notation there is implicit collection of the code

Translating if-then-else

- Similar to while except that code is slightly more complicated because if-then-else must reach a merge and the else branch is optional.

$[\![C;rt \vdash \texttt{if (}e_1\texttt{) } s_1 \texttt{ else } s_2 \Rightarrow C']\!] = [\![C']\!]$

```
    opn = ⟦C ⊢ e : bool⟧
    %test = icmp eq i1 opn, 0
    br %test, label %else, label %then
then:
    ⟦C;rt ⊢ s1 ⇒ C'⟧
    br %merge
else:
    ⟦C; rt s2 ⇒ C'⟧
    br %merge
merge:
```

Connecting this to Code

- Instruction streams:
  - Must include labels, terminators, and “hoisted” global constants
- Must post-process the stream into a control-flow-graph
- See frontend.ml from HW4

OPTIMIZING CONTROL

Standard Evaluation

- Consider compiling the following program fragment:

```
if (x & !y | !w)
  z = 3;
else
  z = 4;
return z;
```

```
    %tmp1 = icmp Eq [[y]], 0        ; !y
    %tmp2 = and [[x]] [[tmp1]]
    %tmp3 = icmp Eq [[w]], 0
    %tmp4 = or %tmp2, %tmp3
    %tmp5 = icmp Eq %tmp4, 0
    br %tmp4, label %else, label %then

then:
    store [[z]], 3
    br %merge

else:
    store [[z]], 4
    br %merge

merge:
    %tmp5 = load [[z]]
    ret %tmp5
```

Observation

- Usually, we want the translation ⟦e⟧ to produce a value
  - ⟦C ⊢ e : t⟧ = (ty, operand, stream)
  - e.g. ⟦C ⊢ e_1 + e_2 : int⟧ = (i64, `%tmp`, [`%tmp = add` ⟦e_1⟧ ⟦e_2⟧])

- But when the expression we're compiling appears in a test, the program jumps to one label or another after the comparison but otherwise never uses the value.

- In many cases, we can avoid "materializing" the value (i.e. storing it in a temporary) and thus produce better code.
  - This idea also lets us implement different functionality too: e.g. short-circuiting boolean expressions

Idea: Use a different translation for tests

Usual Expression translation:

⟦C ⊢ e : t⟧ = (ty, operand, stream)

Conditional branch translation of booleans, without materializing the value:

⟦C ⊢ e : bool@⟧ ltrue lfalse = stream

⟦C, rt ⊢ if (e) then s1 else s2 ⇒ C′⟧ = ⟦C′⟧,

```
    insns3
then:
    ⟦s1⟧
    br %merge
else:
   ⟦s2⟧
    br %merge
merge:
```

Notes:

- takes two extra arguments: a "true" branch label and a "false" branch label.
- Doesn't "return a value"
- Aside: this is a form of continuation-passing translation…

where

⟦C, rt ⊢ s_1 ⇒ C′⟧ = ⟦C′⟧, $insns_1$

⟦C, rt ⊢ s_2 ⇒ C″⟧ = ⟦C″⟧, $insns_2$

⟦C ⊢ e : bool@ ⟧ `then else` = $insns_3$

Short Circuit Compilation: Expressions

- ⟦C ⊢ e : bool@⟧ ltrue lfalse = insns

$$\frac{}{⟦C ⊢ \texttt{false} : \texttt{bool@}⟧\ \texttt{ltrue}\ \texttt{lfalse} = \texttt{[br \%lfalse]}}\ \text{FALSE}$$

$$\frac{}{⟦C ⊢ \texttt{true} : \texttt{bool@}⟧\ \texttt{ltrue}\ \texttt{lfalse} = \texttt{[br \%ltrue]}}\ \text{TRUE}$$

$$\frac{⟦C ⊢ e : \texttt{bool@}⟧\ \texttt{lfalse}\ \texttt{ltrue} = \texttt{insns}}{⟦C ⊢ \texttt{!e} : \texttt{bool@}⟧\ \texttt{ltrue}\ \texttt{lfalse} = \texttt{insns}}\ \text{NOT}$$

Short Circuit Evaluation

Idea: build the logic into the translation

$$\frac{[\![C \vdash e1 : bool@]\!]\ \ltrue\ right = insns_1 \quad [\![C \vdash e2 : bool@]\!]\ \ltrue\ lfalse = insns_2}{[\![C \vdash e1 \,|\, e2 : bool@]\!]\ \ltrue\ lfalse =}$$

```
    insns1
right:
    insn2
```

$$\frac{[\![C \vdash e1 : bool@]\!]\ right\ lfalse = insns_1 \quad [\![C \vdash e2 : bool@]\!]\ \ltrue\ lfalse = insns_2}{[\![C \vdash e1\&e2 : bool@]\!]\ \ltrue\ lfalse =}$$

```
    insns1
right:
    insn2
```

where `right` is a fresh label

Short-Circuit Evaluation

- Consider compiling the following program fragment:

```
if (x & !y | !w)
  z = 3;
else
  z = 4;
return z;
```

```
    %tmp1 = icmp Eq [[x]], 0
    br %tmp1, label %right2, label %right1

right1:
    %tmp2 = icmp Eq [[y]], 0
    br %tmp2, label %then, label %right2

right2:
    %tmp3 = icmp Eq [[w]], 0
    br %tmp3, label %then, label %else

then:
    store [[z]], 3
    br %merge

else:
    store [[z]], 4
    br %merge

merge:
    %tmp5 = load [[z]]
    ret %tmp5
```