#### **Exam Review**

Computer Operating Systems, Fall 2023

Instructor: Travis McGaha

**Head TAs:** Nate Hoaglund & Seungmin Han

#### TAs:

Andy Jiang	Haoyun Qin	Kevin Bernat	Ryoma Harris
Audrey Yang	Jason hom	Leon Hertzberg	Shyam Mehta
August Fu	Jeff Yang	Maxi Liu	Tina Kokoshvili
Daniel Da	Jerry Wang	Ria Sharma	Zhiyan Lu
Ernest Ng	Jinghao Zhang	Rohan Verma	

#### Administrivia

- Reach out to TA's to schedule PennOS Demo ASAP
  - Today and tomorrow are the last days to demo
  - You should use the version of PennOS you submitted unless you got prior approval to use one with small bug fixes.

- Exam is Thus 7-9pm in Meyerson B1
  - Exam policies and review materials will be posted after lecture.

#### Administrivia

- Two things due before Reading Days, will be released after the exam
  - Check-in (Short survey), done anonymously and pass/fail
  - Team Evaluation for PennOS
    - Pass/fail
    - Done individually, you will describe how much and what everyone contributed to pennos
    - We will use this to handle cases where there was a large imbalance in the work done.
    - If there are big inconsistencies between team members, we will investigate



pollev.com/tqm

Any questions, comments or concerns from last lecture?

### Midterm Philosophy / Advice (pt. 1)

- I do not like midterms that ask you to memorize things
  - You will still have to memorize some critical things.
  - I will hint at some things, provide documentation or a summary of some things. (for example: I will provide parts of the man pages for various system calls)
- I am more interested in questions that ask you to:
  - Apply concepts to solve new problems
  - Analyze situations to see how concepts from lecture apply
- Will there be multiple choice?
  - If there is, you will still have to justify your choices

#### Midterm Philosophy / Advice (pt. 2)

- I am still trying to keep the exam fair to you, you must remember some things
  - High level concepts or fundamentals. I do not expect you to remember every minute detail.
    - E.g. how a multi level page table works should be know, but not the exact details of what is in each page table entry
    - (I know this boundary is blurry, but hopefully this statement helps)
- I am NOT trying to "trick" you (like I sometimes do in poll everywhere questions)

#### Midterm Philosophy / Advice (pt. 3)

- I am trying to make sure you have adequate time to stop and think about the questions.
  - You should still be wary of how much time you have
  - But also, remember that sometimes you can stop and take a deep breath.
- Remember that you can move on to another problem.
- Remember that you can still move on to the next part even if you haven't finished the current part

### Midterm Philosophy / Advice (pt. 4)

- On the midterm you will have to explain things
- Your explanations should be more than just stating a topic name.
- Don't just say something like (for example) "because of threads" or just state some facts like "threads are parallel and lightweight processes".
- State how the topic(s) relate to the exam problem and answer the question being asked.

#### Disclaimer

## \*THIS REVIEW IS NOT EXHAUSTIVE

# Topics not in this review are still testable

Recitation after lecture is exam review

#### **Lecture Outline**

- Processes vs Threads
- Memory Allocation
- Caches
- Scheduling
- File System Block Allocation
- RAID
- Threads & Data Races
- Deadlock

#### **Processes vs Threads**

- Let's say we had a program that did an expensive computation we wanted to parallelize, we could use either threads or processes. Which one would be faster and why?
- Sometimes we want to call software that is written in another language. If it is written as a library with the proper support (e.g. TensorFlow is in C++ but callable from Python), we could use threads. If we want to invoke a program that is already compiled (isn't a library/doesn't have a callable interface) we could not use threads. We would have to use fork & exec. Why?

#### **Memory Allocation**

Assume we have the following two pieces of code, which ones is likely faster than the other and why?

```
#include <stdio.h>
#include <stdlib.h>

int main(int argc, char** argv) {
    int* arr = malloc(sizeof(int) * 10);
    arr[0] = 1;
    arr[1] = 1;
    for(int i = 2; i < 10; i++) {
        arr[i] = arr[i-1] + arr[1-2];
    }
    printf("%d\n", arr[9]);
    free(arr);
}</pre>
```

```
#include <stdio.h>
#include <stdlib.h>
```

```
int main(int argc, char** argv) {
    int arr[10];
    arr[0] = 1;
    arr[1] = 1;
    for (int i = 2; i < 10; i++) {
        arr[i] = arr[i-1] + arr[1-2];
    }
}</pre>
```

```
printf("%d\n", arr[9]);
free(arr);
```

#### **Memory Allocation**

- Lets say that in addition to malloc, we also had a custom slab allocator implemented that could allocate chunks of space that is 64 bytes (16 integers) large.
- What is one reason we may prefer the custom slab allocator to malloc?

What is one reason we may prefer malloc?

#### **Memory Allocation**

How is the array in this snippet of code likely allocated at a low level (in assembly)? [#include <stdio.h>

```
#include <stdlib.h>
int main(int argc, char** argv) {
    int arr[10];
    arr[0] = 1;
    arr[1] = 1;
    for (int i = 2; i < 10; i++) {
        arr[i] = arr[i-1] + arr[1-2];
    }
    printf("%d\n", arr[9]);
}</pre>
```

#### Caches

 The most common way to store a sequence of elements in C++ and most languages is a dynamically resizable array (e.g. a vector).

A vector of <int> looks something like this in memory:



#### Caches

- Typically, a bool variable is 1 byte. How much space does a bool strictly need though?
  - 1 bit
- C++ goes against the standard implementation of a vector for the bool type, and instead has each bool stored as a bit instead of the type a stand-a-lone Boolean variable would be stored as.
  - Travis thinks this was a horrible design decision, but there is a reason why they did this. What are those reasons?

#### Caches

- If we stored a vector of 120 bools, and wanted to iterate over all of them, roughly how many cache hits & misses would we have if we:
  - You can assume a cache line is 64 bytes.
  - If we used a vector<bool> that allocates the bools normally (1 byte per bool)

If we use a vector<bool> that represents each bool with a single bit

#### Scheduling

Four processes are executing on one CPU following round robin scheduling:



- You can assume:
  - All processes do not block for I/O or any resource.
  - Context switching and running the Scheduler are instantaneous.
  - If a process arrives at the same time as the running process' time slice finishes, the one that just arrived goes into the ready queue before the one that just finished its time slice.

#### Scheduling



- All processes do not block for I/O or any resource.
- Context switching and running the Scheduler are instantaneous.
- If a process arrives at the same time as the running process' time slice finishes, the one that just arrived goes into the ready queue before the one that just finished its time slice.
- What is the earliest time that process C could have arrived?
- Which processes are in the ready queue at time 9?
- If this algorithm used a quantum of 3 instead of 2, how many fewer context switches would there be?

#### **File System Block Allocation**

- Consider that we want to read the 5th block of the file /home/me/.bashrc, what is the worst-case number of disk blocks that must be read in for each of the following:
  - You can assume a block is 4096 bytes
  - assume that directory entries we are looking for are in the firs block of each directory we search
- Linked List Allocation
  - Assume we know the block number of the first block in root dir
- Linked List Allocation via FAT
  - Assume we know where the root directory starts in the FAT.
  - You can also assume a FAT entry is 2 bytes.
- I-nodes
  - assume we know where the I Node for the root directory is

#### **File System Block Allocation**

How does the numbers change if we instead wanted to write to the 5<sup>th</sup> block of the file?

Despite not having the best numbers, I nodes are still chosen over FAT. Why is this the case?

#### RAID

- You are deciding between RAID 0, RAID 1, RAID 4 and RAID 5 for a system you are working on.
  - Assume we have 10 Disks available to us and a parity to data ratio of 1:4.
- Which RAID level allows for the most possible parallel reads? Which one provides the least? Why?
- What if we wanted to see which RAID level provides the most parallel writes? Which one provides the least? Why?

#### RAID

Suppose that we took RAID 5 and had parity blocks per stripe instead of 1. Each of the two parity blocks use a different algorithm to calculate them in such a way that makes the system tolerant to two disk failures instead of 1.

What are two downsides of this model compared to RAID level 5?

#### **Threads & Data Races**

- \* Consider the following pseudocode that uses threads. Assume that file.txt is large file containing the contents of a book. Assume that there is a main() that creates one thread running first\_thread() and one thread for second\_thread()
- There is a data race.
   How do we fix it using just a mutex?
   (where do we add call

```
while (!f.eof()) {
    string data_read = f.read(10 chate data = data_read;
    }
}
void* second_thread(void* arg) {
    while (true) {
        if (data.size() != 0) {
            print(data);
        }
        data = "";
    }
}
```

(where do we add calls to lock and unlock?)

#### **Threads & Data Races**

After we remove the data race on the global string, do we have deterministic output? (Assuming the contents of the

file stays the same).

```
void* first thread(void* arg) {
  f = open("file.txt", O RDONLY);
  while (!f.eof()) {
     string data read = f.read(10 chars);
     data = data read;
  }
}
void* second thread(void* arg) {
  while (true) {
    if (data.size() != 0) {
      print(data);
    data = "";
```

string data = ""; // global

#### **Threads & Data Races**

- There is an issue of inefficient CPU utilization going on in this code. What is it and how can we fix it?
- (You can describe the fix at a high level, no need to write code)

```
string data = ""; // global
void* first thread(void* arg) {
  f = open("file.txt", O RDONLY);
  while (!f.eof()) {
     string data read = f.read(10 chars);
     data = data read;
  }
}
void* second thread(void* arg) {
  while (true) {
    if (data.size() != 0) {
      print(data);
    data = "";
```

#### Deadlock

- Consider we are working with a data base that has N numbered blocks. Multiple threads can access the data base and before they perform an operation, the thread first acquires the lock for the blocks it needs.
  - Example: Thread1 accesses B3, B5 and B1. Thread2 may want to access B3, B9, B6. Here is some example pseudo code:

```
void transaction(list<int> block_numbers) {
  for (every block_num in block_numbers) {
    acquire_lock(block_num)
  }
  operation(block_numbers);
  for (every block_num in block_numbers) {
    release_lock(block_num);
  }
}
```

#### Deadlock

- This code has the possibility to deadlock. Give an example of this happening. You can assume no thread tries to acquire the same lock twice
- Someone proposes we fix this by locking the whole database instead of locking at the block level. What downsides does this have? Does it even avoid deadlocks?
- How can we fix this (without locking the whole database if that even works)?

```
void transaction(list<int> block_numbers) {
  for (every block_num in block_numbers) {
    acquire_lock(block_num)
  }
  operation(block_numbers);
  for (every block_num in block_numbers) {
    release_lock(block_num);
  }
}
```